

**Department of Electrical and Electronics Engineering (EEE)
BITS, Pilani, Pilani Campus**

Admission to the Ph.D. program, SEM-II 2021-22

1. Research area(s) for Ph. D. admission are listed below,

Sl.No.	Thrust Research Areas	Position available	
		Full-Time Scheme	Part-Time Scheme
1	Instrumentation & Control	Yes	Yes
2	Communication Systems & Networks	Yes	Yes
3	RF, Microwave, Antenna Design & Wireless system	Yes	Yes
4	Power Systems, Electrical Energy, Renewable Energy & Smart Grids	Yes	Yes
5	Power Electronics & Drives	Yes	Yes
6	IoT and Embedded Systems	Yes	Yes
7	Micro / Nano Electronics & VLSI	Yes	Yes
8	Electronic Materials, Devices & Technology	Yes	Yes
9	Digital Signal Processing	Yes	Yes

Note: Students with own funding/scholarship (UGC, CSIR, etc) can apply irrespective of the available positions shown in the table above. They can directly contact the Head of Department (Prof. H.D. Mathur, hod.eee@pilani.bits-pilani.ac.in) or DRC convener (Prof. Praveen Kumar A.V, praveen.kumar@pilani.bits-pilani.ac.in) for related queries.

2. Ph.D. Admission Schemes Offered:

(a) Full-Time Scheme

Preferably for individuals who would like to pursue Ph.D. in-house, residing on campus. Such students will work full-time towards Ph.D. and are eligible for institute fellowship.

(b) Part-time Scheme: Individuals working in academic / research organizations having proper environment and facilities for conducting research will be admitted under this scheme. Such candidates are not entitled to financial support from the Institute. Part-time students will have to visit the campus for course registration, semester evaluations (both mid-semester and end semester), and as per the requirement from the supervisor / competent authority. They will also be required to submit NOC from the parent organization at the time of admission.

3. Institute Fellowship / Financial Assistance:

Full-time students will receive Rs. 28,000-31,000 per month, based on institute policy on the highest degree the student holds. After completion of two years, the fellowship may be increased to either Rs. 33,000 or Rs. 35,000 per month based on an internal assessment of the research outcomes. Additionally, annual contingency and travel grants to attend workshops and conferences will also be provided.

4. Eligibility and Shortlisting:

Good academic record (70 % marks or CG = 7/10) in the following,

- (a) **M.E/M.Tech.** in relevant area (Table in page 1) or,
- (b) **M. Sc.** in Electronics / Electronics Science / Physics with Electronics, etc., **with own funding** (CSIR, UGC etc.)

The selection is based on the initial screening of the application, followed by a technical interview. DRC will shortlist potential candidates for the interview based on the preferred research area, academic background, and application's merit. The interview schedule for shortlisted candidates will be published in the BITS admission website. No TA/DA will be provided for attending the interview.

5. Fee Structure:

Visit <https://www.bitsadmission.com/phdmain.aspx> for updates.

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